

Docket No.: 60188-137

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Toru IWATA, et al.

Serial No.:

Filed: January 08, 2002

For: CLOCK RECOVERY CIRCUIT

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: Group Art Unit:
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: Examiner:
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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.


This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Respectfully submitted,

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 60188-137		SERIAL NO.			
				APPLICANT Toru IWATA, et al.				1c997 U.S. PTO 10/038613 	
				FILING DATE January 08, 2002		GROUP			
U.S. PATENT DOCUMENTS									
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE			
	6,064,248	5/16/2000	Seki						
FOREIGN PATENT DOCUMENTS									
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation			
						Yes	No		
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)									
	D.H. Wolaver, "Phase-Locked Loop Circuit Design", Section 10-2, pp. 213-216, Prentice Hall (1991)								
	R. Gu, et al., "A 0.5-3.5Gb/s Low-Power Low-Jitter Serial Data CMOS Transceiver", ISSCC Digest of Technical Papers, pp. 352-353, Feb. 1999.								
EXAMINER				DATE CONSIDERED					

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.